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CORRECTED FILING RECEIPT



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**Applicant(s)**

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**Assignment For Published Patent Application**

MATRIX SEMICONDUCTOR, INC.;

**Domestic Priority data as claimed by applicant**

This application is a CIP of 09/662,953 09/15/2000 ABN

**Foreign Applications****If Required, Foreign Filing License Granted:** 02/09/2001**Projected Publication Date:** Not Applicable**Non-Publication Request:** No**Early Publication Request:** No

**Title**

Memory devices and methods for use therewith

**Preliminary Class**

711

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